



ADC 3.3V 12-BIT 1MS/s – AIP-ADC12B1M3VSAR

FEATURES

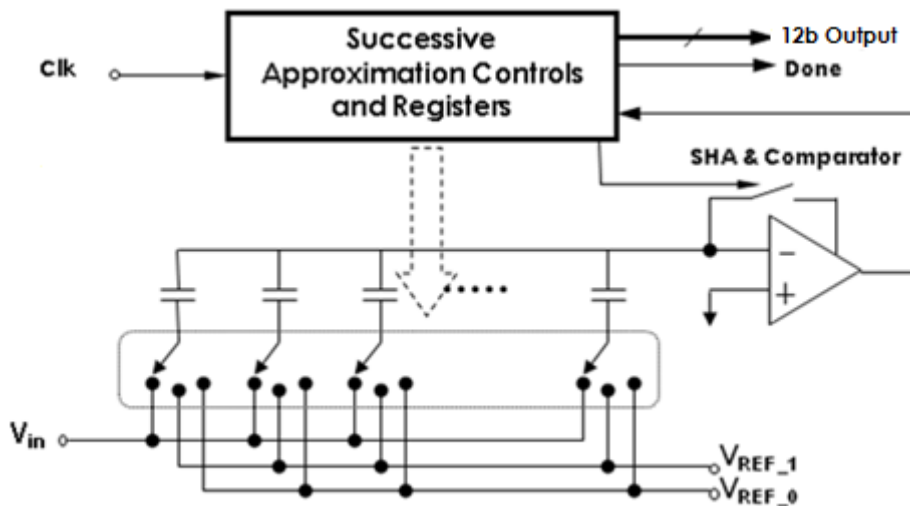
- ▶ Supply voltage: 3.3V
- ▶ Resolution: 12-bit
- ▶ Conversion rate: 1MS/s
- ▶ Input range: 1.5V single-ended
- ▶ Architecture: Successive Approximation
- ▶ INL and DNL: <2LSB and <2LSB
- ▶ Normal and power-down control
- ▶ Power-down mode (consumption <0.1uA)
- ▶ Current consumption: 3.5mA
- ▶ Total core area: NDA required
- ▶ Temperature: -40C to 85C
- ▶ Process technology: 0.35um digital CMOS with MiM

OVERVIEW

- ▶ A 12-bit capacitive successive approximation ADC in standard CMOS process.
- ▶ Fully integrated with internal reference and clock generators.
- ▶ Normal and power down modes.
- ▶ Ideal for handheld devices, medical applications, diagnostic and sensor applications.

IP STATUS

- ▶ MASS PRODUCTION



ASI, INC. CONFIDENTIAL INFORMATION

ASI reserves the right to make changes to the information contained herein without notice.
No liability shall be incurred as a result of its use or application.

www.advsensor.com
contact@advsensor.com
3945 Freedom Circle, Suite 710,
Santa Clara, CA 95054, United States