



AS1120

12 Channels Capacitive Touch Sensor IC

From Santa Clara, United States of America

Leading Performance: *ESD HBM >8k Volts (Directly Applied to All IC Pins)*
Operating Temperature up to >+95°C

Features

- Analog and Digital – Mixed Mode IC
- Support Capacitive Touch Screen Applications
 - Low Cost Single Layer ITO Touch Screen
- Support Capacitive Touch Key Applications
- Support Capacitive Proximity Sensing Applications
- 8 and 12 independent electrode inputs
- 8 and 12 individual outputs (with QFN package)
- Simultaneous multi-electrode sensing
- I2C compatible interface with interrupt IRQ_L
- Selectable I2C address
- Multiple devices in a system allow for up to 24 keys
 - Sensitivity control per key
 - Auto calibration
 - Auto drift compensation
 - Noise filtering and rejection

Reduce BOM Cost

- Internal oscillator – no external crystal or oscillators
- Internal LDO – no external LDO or regulator
- No external tuning components
- Direct drive LED (2mA to 5mA)

Environment Specification

Operation Temperature: -40°C to +95°C

- Leading Performance in Capacitive Touch Sensor IC

ESD: HBM 8k volts (Directly applied to all IC pins)

- Leading Performance in Capacitive Touch Sensor IC

Electrical Specification

Operating voltage: 2.0V to 5.5V

Low-power operating mode: 7uA to 15uA

Standby mode: 5uA

Standby: wakes up to normal mode in less than 32ms

Overview

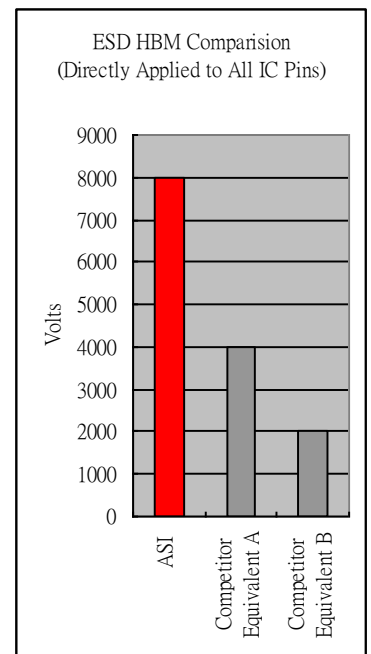
AS1120 is a 12 channels touch sensor IC capable of detecting near-proximity or touch on 12 electrodes. It allows electrodes to project independent sense field through any dielectric like glass, plastic, ceramic and wood. It can accommodate a wide range of implementations due to its rich feature set.

Implementations

- Touch Screen
- Touch Keys
- Touch Pads
- Switch Replacements
- Slider Replacements

Typical Applications

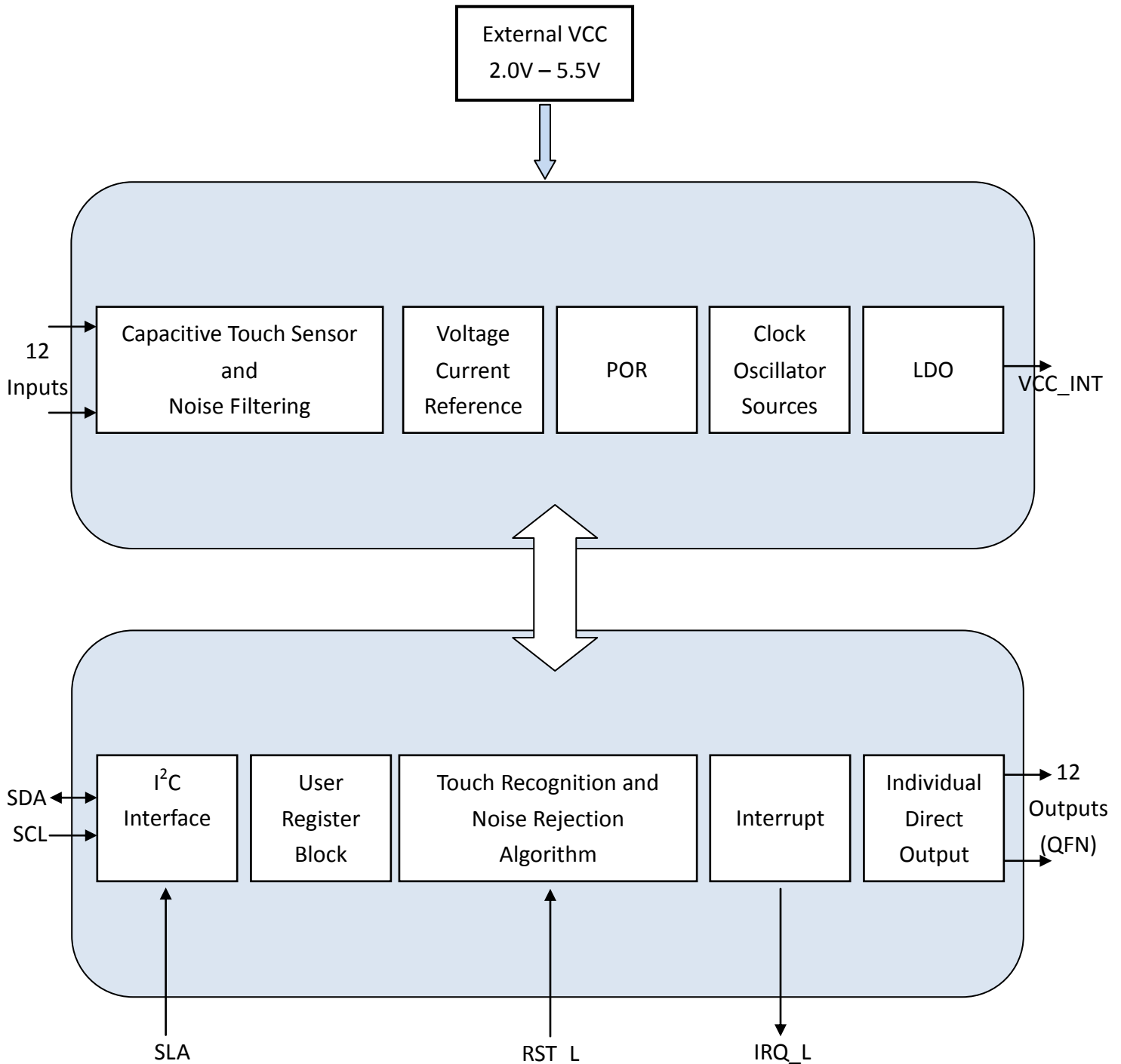
- Audio/Video
- PC peripherals
- Home appliance
- Gaming machine/console
- Security systems
- Instruments panels
- Access system
- Remote controls
- Lighting controls
- Single Layer ITO Glass
- MP3 and MP4



Package Types (Lead Free and Green Packages)

Package Types	Part No.	Channel	(Size mm)
QFN40	AS1120	12	6 x 6 x 0.8
TSSOP24	AS1120	12	7.8 x 4.5 x 1.1

Block Diagram





AS1120

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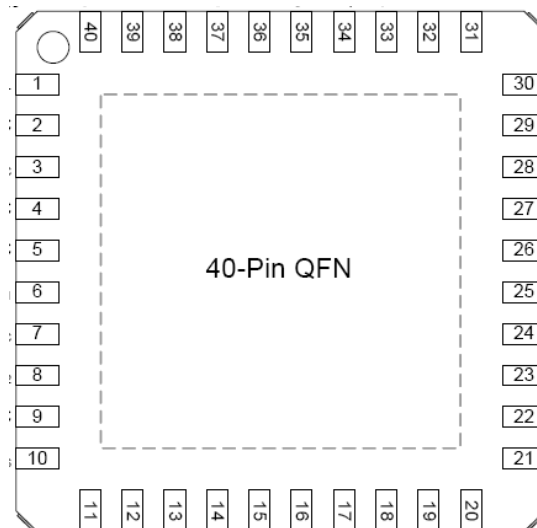
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Pin Assignment and Package

The AS1120 device is available in two types of packages that are listed and shown in the following tables:

40-Pin QFN Part:

Figure 1. AS1120 40-Pin QFN Device



40-Pin QFN Pinout:

Table 1. Pin Descriptions



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Pin Number	Name	Pin Description
3, 2, 38, 37, 33, 32, 29, 28, 1, 36, 34, 30	Key1, Key2, Key3, Key4, Key5, Key6, Key7, Key8, Key9, Key10, Key 11, Key12	Sense pin 1, Sense pin2, Sense pin3, Sense pin4, Sense pin 5, Sense pin6, Sense pin7, Sense pin8, Sense pin 9, Sense pin10, Sense pin11, Sense pin12
5, 6, 7, 15, 17, 18, 25, 26, 8, 14, 23, 24	Out1, Out2, Out3, Out4, Out5, Out6, Out7, Out8, Out9, Out10, Out11, Out12	Key 1 out, Key 2 out, Key 3 out, Key 4 out, Key 5 out, Key 6 out, Key 7 out, Key 8 out, Key 9 out, Key 10 out, Key 11 out, Key 12 out
27	VCC	Power
4	VSS	Ground
13	SCL	SCL – I ² C clock input
12	SDA	SDA – I ² C open drain data I/O pin
16	RST_L	Reset pin, active low
19	IRQ_L	Interrupt Request Output, active low
39	VCC_INT	Power Internal regulator
9	SLA	I ² C slave address selection
35	R_EXT	External Reference Resistor
22	Reserved	Connect to VSS
10, 11, 20, 21, 31, 40	NC	NC – Connect to VSS



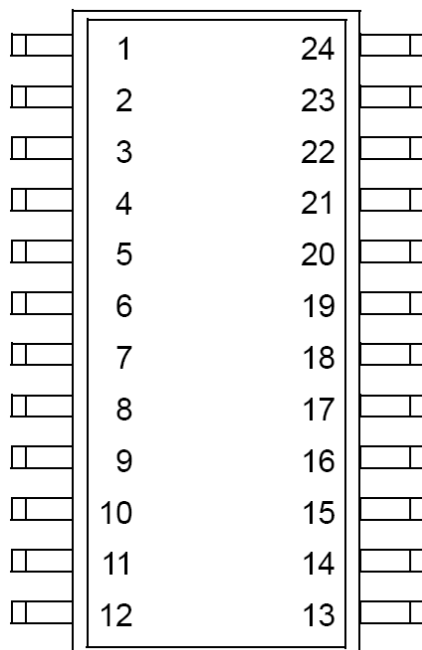
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24-Pin TSSOP Part:

Figure 2. AS1120 24-Pin TSSOP Device



24-Pin TSSOP Pinout:

Table 2. Pin Descriptions

Pin Number	Name	Pin Description
19, 18, 15, 14, 10, 9, 7, 6, 17, 13, 11, 8	Key1, Key2, Key3, Key4, Key5, Key6, Key7, Key8, Key9, Key10, Key 11, Key12	Sense pin 1, Sense pin2, Sense pin3, Sense pin4, Sense pin 5, Sense pin6, Sense pin7, Sense pin8, Sense pin 9, Sense pin10, Sense pin11, Sense pin12
5	VCC	Power
20	VSS	Ground
23	SCL	SCL – I ² C clock input
22	SDA	SDA – I ² C open drain data I/O pin
1	RST_L	Reset pin, active low
2	IRQ_L	Interrupt Request Output, active low
16	VCC_INT	Power Internal regulator
21	SLA	I ² C slave address selection
12	R_EXT	External Reference Resistor
4	Reserved	Connect to VSS
3, 24	NC	NC – Connect to VSS



Technology Overview

Introduction

The ASI Touch sensor devices are charging a sense electrode of unknown capacitance to a known current. The electrode is typically a metal area on a printed circuit board. The resulting potential is transferred into a measurement circuit. By measuring the potential after one or more charge-and-discharge cycles, the capacitance of the sense plate can be determined.

By controlling the charging and discharging current, together with a proprietary sensing method, the result is a very low power consumption operation.

Placing a finger on the touch surface introduces external capacitance. This represents as a finger touch or as proximity detection. Signal processing in the decision logic makes ASI Touch sensor robust and reliable. False triggering due to AC power noise, electrostatic spikes or sudden unintentional touch or proximity is eliminated.

ASI Touch sensors provide truly independent single or multiple keys sensing. Where multiple keys are used, each key is individually sensed and measured. Each key can be set for an individual sensitivity level. Keys of different sizes and shapes can be used to meet different requirements.

ASI Touch sensors can be deployed in many ways, normal or 'touch' mode and high-sensitivity or 'proximity' mode. The highly sensitive proximity sensing is used to detect an end-user's approaching finger, and have the user interface interrupt the electronic equipment or electrical appliance to initiate a system function. The sensors can also be programmed into a 'toggle' mode, which is very useful for implementing a press-ON/press-OFF ON/OFF switch function.

Thanks to the proprietary low power sensing method, the charging pulses are kept at a very low energy level. The benefits of this approach include lower cross-sensor interference, reduced RF emissions and susceptibility.

ASI Touch sensors feature automatic drift compensation to account for slow changes due to ageing or changing environmental conditions. They have a dynamic range of several decades and do not require coils, oscillators, RF components, special cable, RC networks, or a lot of discrete parts. The ASI Touch sensor solution is simple, robust, elegant, and affordable.



Device Overview

Introduction

The AS1120 is a low power, low voltage touch sensor controller which manages up to 12 touch pad electrodes. An I²C interface communicates with the host controller at data rates up to 400kbits/sec. An interrupt output, IRQ_L, is available to advise the host MCU of electrode status changes. For QFN package, there are 12 individual output available for direct drive usage. The AS1120 has an easy to use, sophisticated built-in multi-level touch recognition filtering to detect pad input condition changes due to a true touch without any processing by the application.

The AS1120 Capacitive Touch Sensor IC does not required external components. For maximum EMI rejection in noisy application, an option low cost resister can be added to the input pin.

A bypass capacitor of 1uF should be used between VCC_INT and VSS lines and a 4.7Kohm pull-up resistor should be included on the IRQ_L. The IRQ_L can be used to connect to an external MCU for interrupt request. The remaining connections are SCL and SDA. Depending on the specific application, each of these control lines can be used by connecting them to a host controller. In the most minimal system, the SCL and SDA must be connected to a master I²C interface to communicate with the AS1120.

Serial Interface

The AS1120 uses an I²C Serial Interface. The I²C protocol implementation and the specifics of communicating with the Touch Sensor IC can be referred to the "I²C protocol standard document".

The Slave Address

The AS1120 has a 7-bit long slave address. The bit following the 7-bit slave address (bit eight) is the R/W_L bit, which is low for a write command and high for a read command.

The AS1120 monitors the bus continuously, waiting for a data communication with its slave address. When an AS1120 recognizes its slave address, it acknowledges and is then ready for continued communication.

The AS1120 slave addresses are shown in Table 3.

Table 3. Pin Descriptions

SLA Pin	I ² C Slave Address
SLA pin is Low	7'h6B
SLA pin is High	7'h0D



Functional Overview

Introduction

The AS1120 has an analog front end, digital touch recognition, and a noise rejection algorithm. This data interpretation can be done in many different ways. The AS1120 simplify the usage and shorten the design-in cycle by predefining the sophisticated built-in algorithm. The user can simply fine tune the performance by setting the registers through I²C interface.

Understanding the Basics

AS1120 is a capacitive Touch sensor IC which manages 12 touch pad electrodes. An I²C interface communicates with the host, and an interrupt output advises the host of electrode status changes. The primary application for AS1120 is the management of user interface touch electrodes. Monitoring touch electrodes involves detecting small changes of input capacitance. AS1120 incorporates a self calibration function which continually adjusts the baseline capacitance for each 12 individual channel. Therefore, the host only has to define the detection sensitivity to interpret a touch or release.

AS1120 uses a unique upfront noise filtering capacitance sensor, together with a state machine and a digital signal algorithm engine to analyze the input data and determine whether a key has been touched or released.

AS1120 has very low power consumption in its class for normal operating mode. It can be programmed into pulse mode to reduce the power consumption even further. The AS1120 also offers a standby mode, which draws very minimal power consumption.

Implementation

The AS1120 can be tailored to specific applications by varying the combination of settings: sampling rate, capacitance sensing range, capacitance detector, baseline management system, and the touch detection system.



Absolute Maximum Ratings

Table 5. Absolute Maximum Ratings

Symbol	Description	Min	Typ	Max	Units	Notes
T _{STG}	Storage Temperature	-55	25	+125	C	
T _A	Ambient Temperature with Power Applied	-40	25	+95	C	
V _{CC}	Supply Voltage on V _{CC} relative to V _{SS}	-0.5	-	+6.0	V	
V _{IO}	DC Input Voltage	V _{SS} - 0.5V	-	V _{CC} + 0.5V	V	

Operating Temperature

Table 6. Operating Temperature

Symbol	Description	Min	Typ	Max	Units	Notes
T _A	Ambient Temperature	-40	25	+95	C	
T _J	Junction Temperature	-40	25	+105	C	

DC Characteristics

This section includes information about power supply requirements

Table 5. DC Characteristics (Temperature Range = -40C to 95C Ambient)

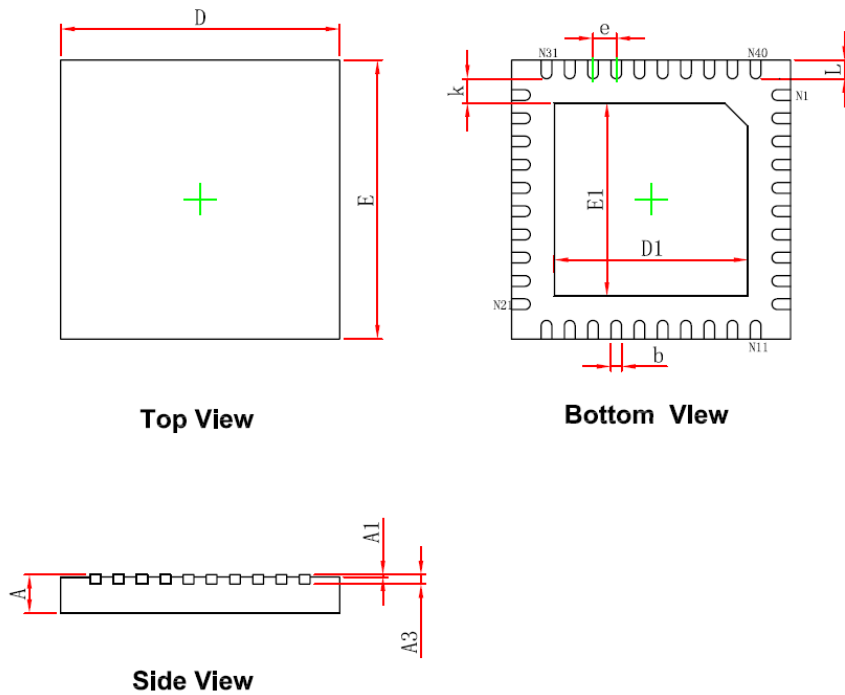
(Typical Operating Circuit, V_{CC} = 2.0V to 5.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise notes. Typical current values are at V_{CC} = 2.5V, T_A = +25C)

Symbol	Description	Min	Typ	Max	Units	Notes
V _{CC}	Operating Supply Voltage	2.0	2.5	5.5	V	
I _{DD}	Average Supply Current		20		μA	Low power mode
I _{DD}	Average Supply Current		38		μA	Normal mode
I _{DD}	Average Supply Current		90		μA	High Speed mode
I _{DD}	Standby Supply Current		4.5		μA	Standby mode
V _{CC}	Supply Voltage on V _{CC} relative to V _{SS}	-0.5	-	+6.0	V	
V _{IH}	Input High Voltage SDA, SCL	0.7V _{CC}			V	
V _{IL}	Input Low Voltage SDA, SCL			0.3V _{CC}	V	
V _{OL}	Output Low Voltage SDA, IRQ_L			0.5	V	I _{OL} = 5mA

Package Dimensions

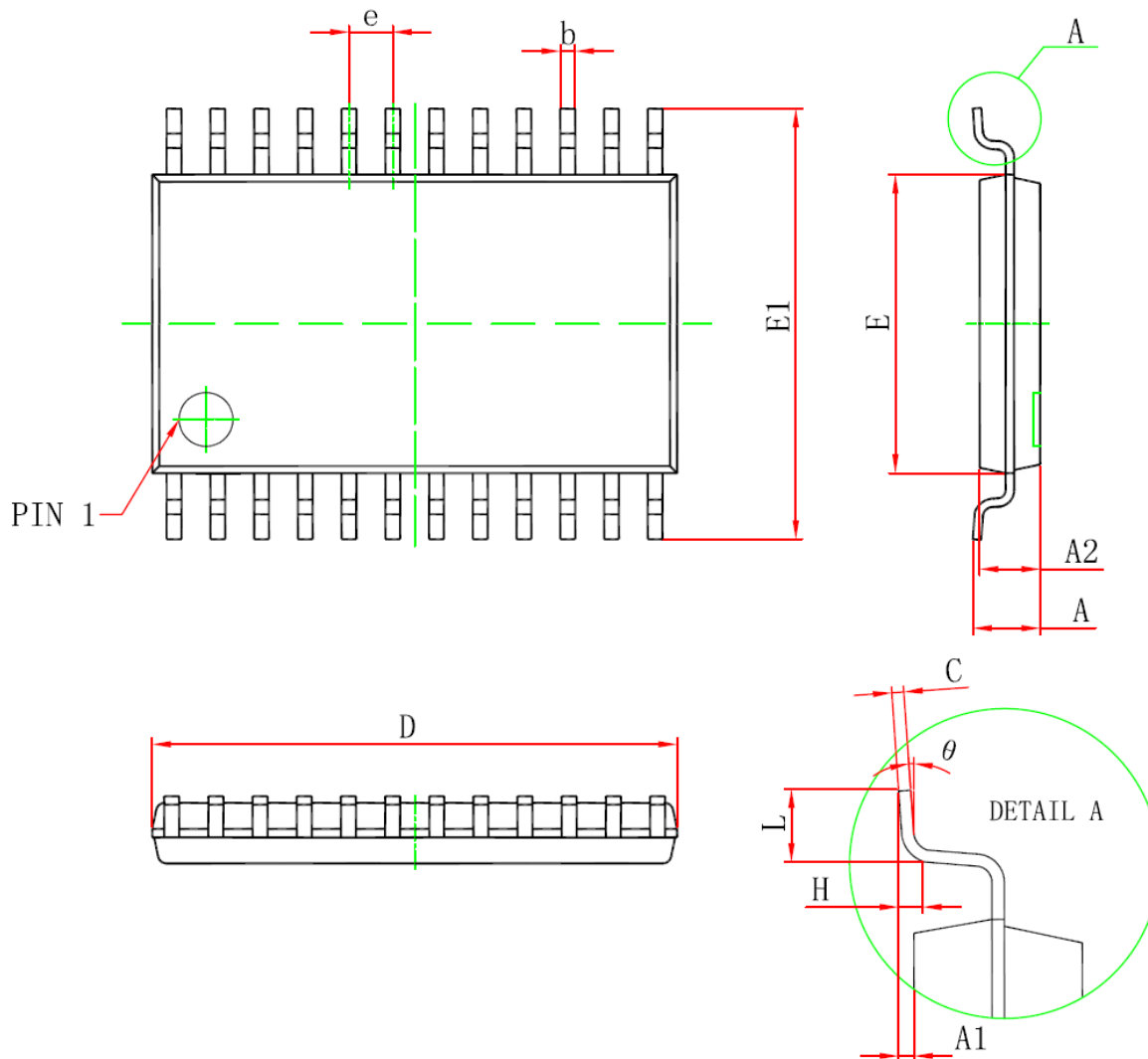
QFN40 PACKAGE OUTLINE DIMENSIONS

QFNWB6×6-40L (P0.50T0.75/0.85) PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	5.900	6.100	0.232	0.240
E	5.900	6.100	0.232	0.240
D1	4.100	4.300	0.161	0.169
E1	4.100	4.300	0.161	0.169
k	0.200MIN.		0.008MIN.	
b	0.180	0.300	0.007	0.012
e	0.500TYP.		0.020TYP.	
L	0.300	0.500	0.012	0.020

TSSOP24 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
D	7.700	7.900	0.303	0.311
E	4.300	4.500	0.169	0.177
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
E1	6.250	6.550	0.246	0.258
A		1.100		0.043
A2	0.800	1.000	0.031	0.039
A1	0.020	0.150	0.001	0.006
e	0.65 (BSC)		0.026 (BSC)	
L	0.500	0.700	0.02	0.028
H	0.25 (TYP)		0.01 (TYP)	
θ	1°	7°	1°	7°